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To: Commissioner for Patents
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From: James C. Kesterson
Reg. No: 25,882
Date: March 15, 2004
Pages: 4

Applicant: Yang, *et al.*
Serial No.: 10/725,234
Docket No: TSM03-0589
Art Unit: 2811


Filed: December 1, 2003
Examiner: TBD

For: Multilayered Dual Bit Memory Device with Improved Write/Erase Characteristics and Method of Manufacturing

Certificate of Transmission under 37 C.F.R. § 1.8

I hereby certify that the following correspondence is being transmitted via facsimile to the Patent and Trademark Office on March 15, 2004.

- Fax Cover Sheet with Certificate of Mailing (1 page)
- Request for Correction (1 page)
- Copy of Filing Receipt with changes marked (2 pages)


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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Yang, *et al.* Docket No.: TSM03-0589
Serial No.: 10/725,234 Art Unit: 2811
Filed: December 1, 2003 Examiner: Unknown
Title: Multilayered Dual Bit Memory Device with Improved Write/Erase
Characteristics and Method of Manufacturing

Office of Initial Patent Examination's
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REQUEST FOR CORRECTION

Petitioner hereby requests a correction in the Filing Receipt of the above-referenced patent application.

The inventor's name, Min Ca, should be spelled, Min Cao. The changes are marked on the attached filing receipt. Please correct the errors noted and send an updated filing receipt.

No fee is believed due in connection with this filing. However, in the event that there are any fees due, please charge the same, or credit any overpayment, to Deposit Account No. 50-1065.

Respectfully submitted,



James C. Kesterson
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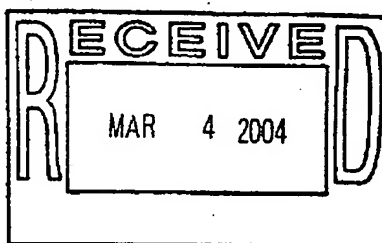
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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/725,234	12/01/2003	2811	1360	TSM03-0589	4	48	4

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CONFIRMATION NO. 2127

FILING RECEIPT



OC000000012001242

Date Mailed: 03/02/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

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Domestic Priority data as claimed by applicant

Foreign Applications

If Required, Foreign Filing License Granted: 02/27/2004

Projected Publication Date: To Be Determined - pending completion of Omitted Items

Non-Publication Request: No

Early Publication Request: No

Title

Multilayered dual bit memory device with improved write/erase characteristics and method of manufacturing

Preliminary Class

257

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